

VHDL Project



Component

École Nationale
Supérieure
d'Électrotechnique
d'Électronique
d'Informatique
d'Hydraulique
et des
Télécommunications

In brief

- **Ametys Code:** N7AE04B
- **Open to exchange students:** No

Presentation

Objectives

- Course illustration: implementation in VHDL of elementary synchronous combinational and sequential functions (counters, registers, multiplexed displays, state machines)
- Ability to apply the knowledge acquired in lectures and practical sessions to design a sequential system described by a specification: Design, VHDL implementation, and testing phase

Pre-requisites

Digital Design Course and Tutorials in VHDL