

Digital design in VHDL



Component

École Nationale
Supérieure
d'Électrotechnique
d'Électronique
d'Informatique
d'Hydraulique
et des
Télécommunications

In brief

- **Ametys Code:** N7AE04A
- **Open to exchange students:** No

Presentation

Description

After learning the basics of the language, we will establish the rules for writing digital circuits. We will analyze practical cases to understand the interactions between the behavioral description in VHDL and the implemented digital circuit. We will illustrate this course using FPGA circuits. 80% of the instruction will be delivered using design tools (VIVADO from Xilinx).

Pre-requisites

Basic knowledge of digital circuits: Combinational circuits, Sequential circuits and State machines.