

ADC and DAC



Component
École Nationale
Supérieure
d'Électrotechnique
d'Électronique

In brief

- > **Ametys Code:** N9EE06B
- > **Open to exchange students:** Yes

Presentation

Objectives

- Design an analog-to-digital converter using CMOS technology.
- Be able to characterize the converter's performance.
- Be familiar with the different converter architectures and their performance.

Description

- The different analog-to-digital converter architectures are presented, along with the parameters used to characterize the converters.
- The diagram of a CMOS technology converter is designed to meet specific specifications and verified by simulation.

Pre-requisites

- Circuit basics
- Transistor circuits
- Operational amplifier circuits

Analog integrated circuits