

Architecture and Assembler-programming



Component

École Nationale
Supérieure
d'Électrotechnique
d'Électronique
d'Informatique
d'Hydraulique
et des
Télécommunications



Semester

Automne

In brief

- > **Amety's Code:** N5EE02B
- > **Open to exchange students:** Yes

Presentation

Objectives

The objective of this course is to enable students to:

- master the simplified operation of a microprocessor
 - understand the operating principle of an operating system.
 - master machine language and assembler
 - produce a simple program in assembler
 - develop a simple microcomputer/system interface
 - master process control by polling
 - understand the general principle of interrupts (sequence breaks)
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Pre-requisites

Mandatory:

- Combinational and Sequential Logic
- Sequential System Synthesis
- Basic Logic Functions (Encoders/Decoders, Multiplexers/Demultiplexers, Counters/Decounters, Registers, etc.)
- Concept of State Machines

Optional: Concepts of Algorithms and Programming Languages.